

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Akhil Garlapati et al.

Title: VOLTAGE REFERENCE GENERATOR CIRCUIT USING LOW-BETA
EFFECT OF A CMOS BIPOLAR TRANSISTOR

Application No.: 10/813,837

Filed:

March 31, 2004

Examiner: Rajnikant B. Patel

Group Art Unit:

2838

Atty. Docket No.: 026-0044

Confirmation No.:

6047

April 12, 2007

Mail Stop 16

Office of Finance, Refund Section

Director of the U.S. Patent and Trademark Office

P.O. Box 1450

Alexandria, VA 22313-1450

REQUEST FOR REFUND UNDER 37 C.F.R. § 1.26

Pursuant to 37 C.F.R. § 1.26, Applicant hereby requests a refund of an overpayment of fees.

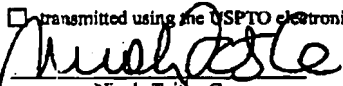
On March 2, 2007, Applicant filed a Supplemental Response after Final Office Action, and mistakenly filed a Petition for Extension of Time (EOT). Payment was made in the amount of \$330.00 for the EOT.

A Response to Final Office Action of September 29, 2006 was timely filed on November 29, 2006. An Advisory Action was mailed January 3, 2007 and a Notice of Appeal with a one-month EOT was filed January 9, 2007. Thus, the EOT for the Supplemental Response of March 2, 2007 was mistakenly paid, and no additional fee was due.


Applicant, therefore, respectfully requests that [REDACTED] be credited the difference between the amount actually paid (\$330) for the EOT and the amount properly due (\$0), thereby refunding the difference (\$330) as soon as possible.

request for refund

PATENT

<u>CERTIFICATE OF MAILING OR TRANSMISSION</u>	
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<input type="checkbox"/>	transmitted using the USPTO electronic filing system.
	<u>4/12/07</u>
Nicole Teitler Cave	Date

Respectfully submitted,



Nicole Teitler Cave, Reg. No. 54,021

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request for refund

ZAGORIN O'BRIEN GRAHAM LLP*Intellectual Property Attorneys*7600B N. CAPITAL OF TEXAS HWY, SUITE 350
AUSTIN, TEXAS 78731**FACSIMILE TRANSMITTAL TO USPTO**

To: Mail Stop 16 Office of Finance Refund Section, USPTO	Fax No.: 571-273-6500
Date: April 12, 2007	Pages: 3 (including this sheet)
From: Nicole Teitler Cave	Sender's Phone No.: 512-338-6300 Sender's Fax No.: 512-338-6301

REGARDING:

Title:	VOLTAGE REFERENCE GENERATOR CIRCUIT USING LOW-BETA EFFECT OF A CMOS BIPOLAR TRANSISTOR
Application No.: 10/813,837	Filed: March 31, 2004
Examiner: Rajnikant B. Patel	Group Art Unit: 2838
Atty. Docket No.: 026-0044	Confirmation No.: 6047

ATTACHED HERETO:

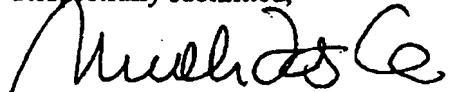
(1) Request for Refund (2 pages)

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Respectfully submitted,


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Electronic Patent Application Fee Transmittal

Application Number:	10813837			
Filing Date:	31-Mar-2004			
Title of Invention:	Voltage reference generator circuit using low-beta effect of a CMOS bipolar transistor			
Refund Ref: 04/25/2007 0030039890				
Credit Card Refund Total: \$330.00				
First Named Inventor/Applicant Name:	Akhil K. Garlapati			
Filer:	Nicole Teitler Cave/Katrina Prati			
Attorney Docket Number:	026-0044			
Filed as Large Entity				
Utility Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				
Extension - 2 months with \$120 paid	1252	1	330	330

Adjustment date: 04/25/2007 SFELEKE1
03/02/2007 INTEFSW 00001429 10813837
01 FC:1252 -330.00 DP

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Total in USD (\$)				330